Call for Papers

3rd Pan-Pacific EMC Joint Meeting *PPEMC'08*

Topics of "EMC on Transmission line and Printed Circuit Board"

May 15-16, 2008 NTT Musashino R&D Center, Tokyo, Japan

Pan-Pacific EMC Joint Meeting - PPEMC'08 - will be held on May 15-16, 2008 at NTT Musashino R&D Center, Tokyo, Japan, co-sponsored by EMCJ in IEICE and IEEE EMC-S Japan and Sendai Chapters.

Symposium Chairman: Yoshio Kami (University of Electro-Communications)

Conference Topics in detail:

- 1) Transmission Lines and Cables
- 2) Coupling and Crosstalk,
- 3) PCB and Circuit Design for EMC Control

Submission Process:

Author's should submit an extended abstract (100 words in English only), reflecting new or advanced study, theory or application. Submit the abstract via **PPEMC08 website**.

http://www.ieice.org/cs/emcj/jpn/international/pan-pacific/index.html

Author's Schedule:

Synopsis Due: 15 March 2008 Acceptance Notification: 31 March 2008 Registration Deadline: 18 April 2008 Final Paper Due: 18 April 2008

The IEICE Transactions on Communications announces a forthcoming IEICE Special Section on 3rd Pan-Pacific EMC Joint Meeting - PPEMC'08- to be published in June 2009.

For further information, please contact to:

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http://www.ntt.co.jp/islab/e/access/index.html