

Call for Papers

2nd Pan-Pacific EMC Joint Meeting *PPEMC'06*

May 25-26, 2006

50th Anniversary Hall, Okayama University
Okayama, Japan

"Let Us Build a New EMC Silk Road in the New Traffic Revolution Era."

Pan-Pacific EMC Joint Meeting – PPEMC'06 – will be held on May 25-26, 2006 at 50th Anniversary Hall of Okayama University, Okayama, Japan, co-sponsored by EMCJ in IEICE and IEEE EMC-S Japan Chapter.

Conference Topics

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| 1) Measurement Techniques | 10) Shielding and Grounding |
| 2) EMC Test Facilities | 11) EM Absorber |
| 3) System-Level EMC | 12) Numerical Modeling |
| 4) Chip and Package Level EMC | 13) Biological Effects and Safety |
| 5) PCB and Circuit Design for EMI Control | 14) EM Environment Measurement |
| 6) Coupling and Crosstalk | 15) Power System EMC |
| 7) Transmission Lines and Cables | 16) Communication System EMC |
| 8) Immunity/Susceptibility | 17) EM-Wave Propagation and Scattering |
| 9) ESD, Lightning and Transients | 18) Others |

Submission Process

Authors should submit an extended abstract (100 words in English only), reflecting new or advanced study, theory or application. Submit the abstract via the **PPEMC website**

<http://www.ieice.org/cs/emcj/jpn/pan-pacific/index.htm>

Author's Schedule:

Synopsis Due:	15 February 2006
Acceptance Notification:	15 March 2006
Registration Deadline:	15 April 2006
Final Paper Due:	15 April 2006

For further information, please contact to

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