2018 Asia-Pacific Workshop on Fundamentals and Applications of Advanced Semiconductor Devices


July 2 – 4, 2018
Hotel NEW TAGAWA, Kitakyushu, Fukuoka, Japan

Call for Papers

The 2018 Asia-Pacific Workshop on Fundamentals and Applications of Advanced Semiconductor Devices (AWAD 2018) will be held at Hotel NEW TAGAWA (Kitakyushu), Fukuoka, Japan, from July 2 (Mon) to 4 (Wed), 2018. This annual workshop will be the 26th in a series that has been held alternately in Japan and Korea since 1993. The purpose of this workshop is to bring together scientists and engineers who are actively engaged in advanced semiconductor devices and materials to discuss the present and future of device processing and related technologies. The workshop will cover the entire field of semiconductor devices and materials, from fundamental physics to recent improvements in device performance and processing technology.

Sponsored by

The Electronics Society of the Institute of Electronics, Information and Communication Engineers of Japan (IEICE-ES), in cooperation with the Institute of Electronics and Information Engineers (IEIE).

Topics to be discussed will include, but not necessarily be limited to, the following areas:

- Advanced integration technologies
- MOS/bipolar devices and technologies
- ULSI process and technologies
- Compound semiconductor materials and devices
- High-speed devices and circuits
- Microwave/Millimeter-wave devices and circuits
- High power devices
- TFT materials, devices and circuits
- Wide bandgap materials and devices
- Quantum effect/single electron devices
- Novel devices and circuits
- Characterization and simulation
- Semiconductor device applications
- Sensors and displays
- Quantum effect/single electron devices
- Novel devices and circuits
- Characterization and simulation
- Sensors and displays
- Semiconductor device applications
- Sensors and displays

Abstract submission

Prospective authors must submit a 100-word abstract with author information via the workshop website no later than March 30 (Fri), April 20 (Fri), 2018.

Technical Digest

The authors of the accepted abstracts will be asked to submit a camera-ready manuscript (2–6 pages, including figures) for the technical digest, which will be distributed to participants at the workshop. Details will be provided to the authors directly. The deadline for manuscript submission is May 25 (Fri), 2018.

Important Dates (JST):

March 30 (Fri), 2018: Deadline for abstract submission
April 20 (Fri), 2018: Deadline for abstract submission (re-extended)
May 25 (Fri), 2018: Deadline for camera-ready manuscript submission

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Special issues in IEICE Transaction on Electronics (IEICE-C) / Journal of Semiconductor Technology and Science (JSTS)

Authors are encouraged to submit their extended manuscript to a special issue in either ‘IEICE Transactions on Electronics (IEICE-C)’ or ‘Journal of Semiconductor Technology and Science (JSTS)’. The submitted papers will undergo a peer-review process before publication. Detailed information will be provided at the workshop and on the website.

Young Researcher Award

The AWAD2018 ‘Young Researcher Award’ and ‘Young Researcher Poster Award’ will be given to outstanding oral/poster presentations authored and presented by young researchers at AWAD2018.

1. Candidates should be the first author and presenter of a student paper (identified by checking "student" in the paper submission form at initial submission). Previous recipients of the ‘Young Researcher Award’ will not be considered.
2. Two Young Researcher Award recipients will be selected by the AWAD committee members based on the quality of the digest manuscript and the oral presentation.
3. Two Young Researcher Poster Award recipients will be selected by the AWAD committee members based on the quality of the poster presentation.
4. Each winner will receive a certificate and a gift.
5. The award ceremony will be held at the closing session on the final day. Each winner will be called for award presentation by the workshop co-chairs.

Venue & Accommodation

For detailed information, visit the workshop website: http://www.ieice.org/~ed/jpn/awad2018/index.html

Language

The official language of the workshop is English.

Registration

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<tr>
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<th>Regular</th>
<th>Student</th>
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<tr>
<td>Early-registration Fee (by June 16th)</td>
<td>¥ 30,000</td>
<td>¥ 12,000</td>
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<tr>
<td>On-site Registration Fee</td>
<td>¥ 35,000</td>
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* Detailed registration information will be available on the workshop website.

Organizing Committee

**Workshop Co-chairs**
- Kunio Tsuda (Toshiba)
- Jungwoo Oh (Yonsei Univ.)

**Workshop Vice-chairs**
- Takahiro Shinada (Tohoku Univ.)
- Hyungtak Kim (Hongik Univ.)

**General Secretaries**
- Masataka Higashiwaki (NICT)
- Woo Young Choi (Sogang Univ.)

**Treasurers**
- Toshihiko Ohishi (Saga Univ.)
- Myounggon Kang (Korea Nat’l Univ. of Transportation)

**Steering Committee Chair**
- Michihiko Suhara (Tokyo Metropolitan Univ.)
- Donghwan Ahn (Kookmin Univ.)

**Local Arrangement Committee**
- Tanemasa Asano (Kyushu Univ.)

**Program Committee Co-chairs**
- Tatsuya Iwata (Toyohashi Univ. Tech.)
- Seongjae Cho (Gachon Univ.)
- Hiroya Ikeda (Shizuoka Univ.)
- Il Hwan Cho (Myongji Univ.)