

ICT FOR THE RESILIENT SOCIETY

PROFESSOR TOSHITAKA TSUDA,
WASEDA UNIVERSITY, JAPAN



For more than 10 years, I have been working towards the realization of the “Human Centric ICT System”, which is now becoming the reality. On March 11th, 2011, Japan was attacked sever disaster named Eastern Japan earthquake and Tsunami, and about 20,000 people lost their lives. ICT system was also damaged, and made it difficult to carry out quick restoration and recovery process. This strongly reminded ICT engineers that the ICT system is the life line of the society, and many discussions started about “How the ICT system should be”. I have been

deeply involved to one of the committee activities whose theme is to realize an ICT system to contribute creating a safe and secure resilient society. The direction of the system paradigm is in good match with the Human Centric ICT System. In my talk, I will introduce the major outcomes of the committee activities together with the concept of the Human Centric ICT System.

Toshitaka Tsuda received BS, MS, and PhD degrees in electrical engineering from the University of Tokyo in 1970, 1972 and 1975, respectively. He joined Fujitsu Laboratories Ltd. in 1975, where he was engaged in R&D of digital signal processing, DSP LSI development, video coding, ISDN subscriber loop transmission systems, broadband ISDN, photonic network systems, packet switched network, and micro processor architecture with LSI development. From 1978 to 1979, he was with the University of California, Berkeley, as a research associate. In 2000, he was assigned as Member of the Board of Fujitsu Laboratories Ltd, Senior Vice President

in 2005, and Fellow in 2010. He also worked as Managing Director of Fujitsu Laboratories of Europe and the Chairman of the board of Fujitsu Laboratories of America. In 2012 he joined Waseda University and teaching ICT service platform architecture and technologies. He gave several keynote speeches in IEEE conferences, ICC2011 and NOMS2012 being the recent examples. He is a Fellow member of IEEE, and honorary member and Fellow of IEICE. He worked as a President of IEICE in 2010. Currently, he is the IEEE Tokyo section chairman. .