

A New memristor Based Chaotic System

Yuxia Li^{*,‡}, Lanying Zhao, Wenqing Chi, Shuli Lu, Xia Huang

Key Laboratory for Robot and Intelligent Technology of Shandong Province,
 College of Information & Electrical Engineering, Shandong
 University of Science and Technology, Qingdao 266510, P. R. China

* Corresponding author

[‡] Email: yuxiali2004@yahoo.com.cn

Abstract: In this work, we present a new memristor based chaotic circuit, which is obtained by replacing the nonlinear resistor in the canonical Chua's circuit with a charge-controlled memristor. This chaotic circuit uses only the four basic circuit elements, and has only one negative element in addition to the nonlinearity. The existence of the chaos is not only demonstrated by computer simulations, but also verified with Lyapunov exponents, bifurcation, poincaré mapping and power spectrum analysis.

Keywords: memristor; chaotic circuit; Chua's diode; charge-controlled.

1. Introduction

Resistor R, capacitor C, and an inductor L are the three well-known basic two-terminal circuit elements. From the circuit theoretic point of view, the memristor (short for memory resistor), with memristance M, was postulated as a fourth fundamental element based on symmetry principle by Leon O. Chua in 1971 [1]. Almost 40 years later, Strukov et al., a team led by R. Stanley Williams from the Hewlett-Packard Company claimed their invention of a physical memristor device together with a useful physical model of the memristor [2], and thereby cemented its place as the fourth circuit element. This nanometer-size solid-state two-terminal passive device has generated worldwide interest because of its potential applications [3], much research is already focused on them [4-8]. Since a memristor is a fundamental circuit element, circuit applications of memristors are also active

topics of research [9-13].

By definition, a memristor is said to be charge-controlled if the nonlinear relation between the charge q and the flux φ can be expressed as a single-valued function $\varphi = \varphi(q)$ of the charge q .

In this paper, we assume that the charge-controlled memristor is characterized by a smooth continuous cubic monotone-increasing nonlinearity as follows:

$$\varphi(q) = aq + bq^3 \quad (1)$$

Consequently, the memristance $M(q)$ are defined by

$$M(q) = \frac{d\varphi(q)}{dq} = a + 3bq^2 \quad (2)$$

2. The new chaotic circuit with charge-controlled memristor and system equations

Figure 1 shows a chaotic circuit with charge-controlled memristor, which is a dual circuit of the canonical Chua's oscillator [9, 13] by replacing the Chua's diode with a charge-controlled memristor. The charge-controlled memristor shown in Fig. 1 is a passive two-terminal electronic device described by Eq. (1).

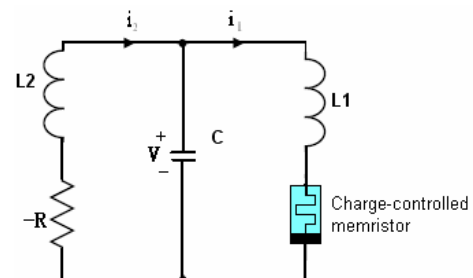


Fig. 1. Chaotic circuit with a charge-controlled memristor

Applying Kirchhoff's voltage laws and current laws to the circuit in Fig.1, we obtain a set of four first-order differential equations, which define the relation among the four circuit variables i_1, i_2, v, q :

$$\begin{cases} L_1 \frac{di_1}{dt} = v - M(q)i_1 \\ C \frac{dv}{dt} = i_2 - i_1 \\ L_2 \frac{di_2}{dt} = R i_2 - v \\ \frac{dq}{dt} = i_1 \end{cases} \quad (3)$$

where the φ - q characteristic curve of the charge-controlled memristor is given by Eq. (1) and $M(w) = \frac{d\varphi(w)}{dw}$.

By let in $x = i_1$, $y = v$, $z = i_2$, $C = 1$, $w = q$, $m = 1/L_1$, $n = 1/L_2$, $k = R/L_2$, and defining the nonlinear functions $\varphi(w)$ and $M(w)$ as

$$\varphi(w) = a w + b w^3 \quad (4)$$

$$M(w) = \frac{d\varphi(w)}{dw} = w + 3b w^2 \quad (5)$$

State equations of (3) can be written in dimensionless form with a time scale factor k as follows:

$$\begin{cases} \frac{dx}{dt} = m(y - M(w)x) \\ \frac{dy}{dt} = z - x \\ \frac{dz}{dt} = kz - ny \\ \frac{dw}{dt} = x \end{cases} \quad (6)$$

where $m, k, n > 0$.

Let $m = 4, n = 1, a = -0.59 \times 10^{-3}, b = 0.02 \times 10^{-3}$, and $k = 0.83$. For initial conditions $(0, 10^{-10}, 0, 0)$, the system(6) is chaotic with Lyapunov exponents $\lambda_1 = 0.1506, \lambda_2 = 0, \lambda_3 = -0.0003, \lambda_4 = -8.7096$, and the Lyapunov dimension is $dL = 3.0179$, the corresponding attractor is depicted in Fig.5.

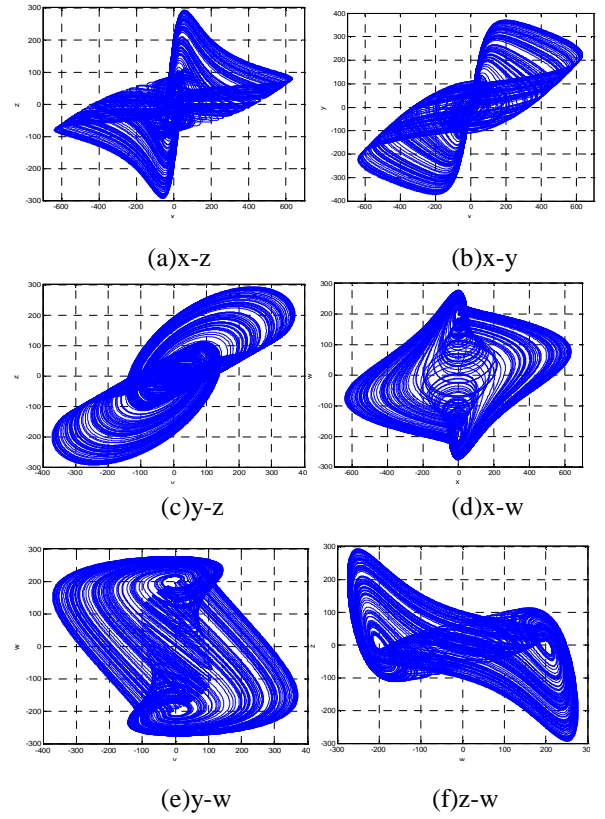


Fig. 2 Phase portraits of the chaotic system (6)

3. Bifurcation analysis of the new system (6)

The spectrum of Lyapunov exponents on the $(k-\lambda)$ plane is obtained as shown in Fig.3, while the corresponding bifurcation diagram of state x with respect to k is given in Fig.4.

It can be observed that the bifurcation diagram well coincides with the spectrum of the Lyapunov exponents. As k increases, system(4) undergoes the following route:

- 1) $0.5 < k \leq 0.6$, $\lambda_1 = 0, \lambda_2, \lambda_3, \lambda_4 < 0$; system(6) is periodic (Fig.5(a)).
- 2) $0.6 < k \leq 0.715$, $\lambda_1 = \lambda_2 = 0, \lambda_3, \lambda_4 < 0$; system(6) is quasi-periodic (Fig.5(b)).
- 3) $0.715 < k \leq 0.76$, $\lambda_1 > 0, \lambda_2 = 0, \lambda_3, \lambda_4 < 0$; system(6) is chaotic (Fig.5(c)).
- 4) $0.76 < k \leq 0.775$, $\lambda_1 = \lambda_2 = 0, \lambda_3, \lambda_4 < 0$; system(6) is quasi-periodic.
- 5) $0.775 < k \leq 0.84$, $\lambda_1 > 0, \lambda_2 = 0, \lambda_3, \lambda_4 < 0$; system(6) is chaotic.

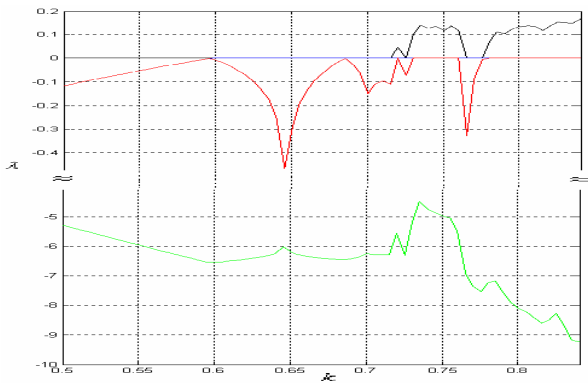


Fig.3 The Lyapunov exponents versus k in the $(k-\lambda_{1,2,3,4})$ plane

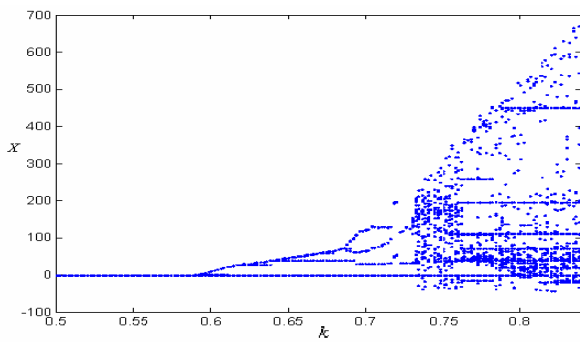


Fig. 4 Bifurcation diagram for increasing k

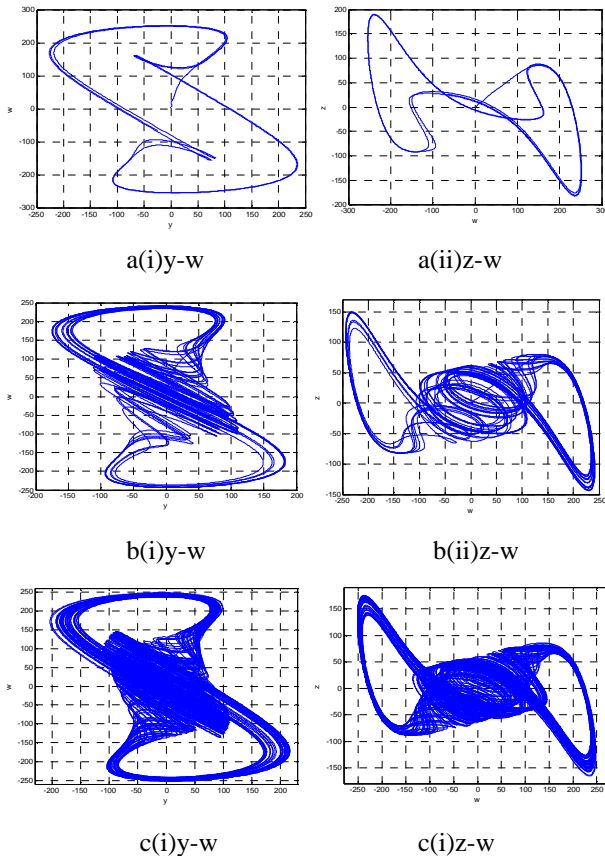
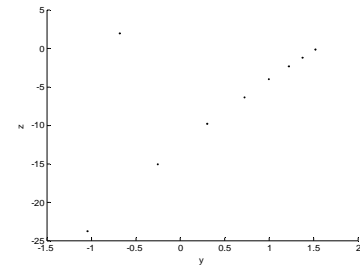


Fig.5 Phase portraits of system (6) in: (i) $y-w$ plane and (ii) $z-w$ plane with different k : (a) period orbit, $k=0.55$; (b) quasi-period orbit, $k=0.65$; (c) chaotic attraction, $k=0.745$; (d) chaotic attraction, $k=0.83$.

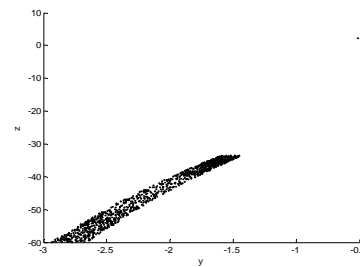
4. Poincaré mapping and power spectrum analysis.

On the Poincaré section, when it has only one point or few dispersed points, the motion is periodic; when it has a closed curve, the motion is quasi-periodic; when it has a lot of concentrated points, the motion is chaotic.

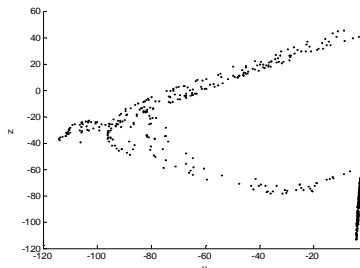
The Poincaré mapping of system (6) with different k is obtained as shown in Fig.6.



(a) period orbit



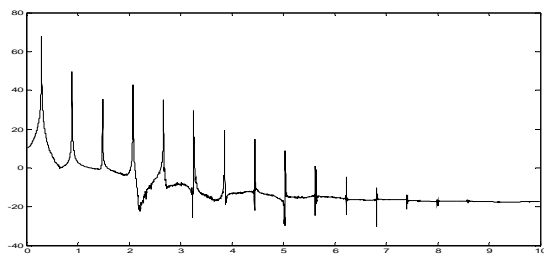
(b) quasi-period orbit



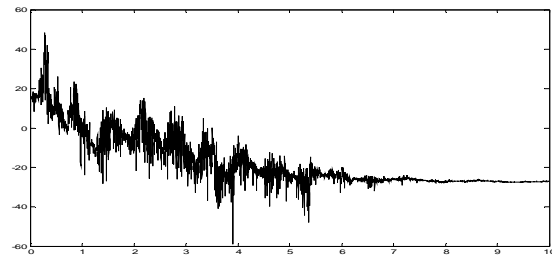
(c) chaotic attraction

Fig. 6 Poincaré mapping of system (6) in: $y-z$ plane with different k : (a) period orbit, $k=0.55$; (b) quasi-period orbit, $k=0.65$; (c) chaotic attraction, $k=0.745$.

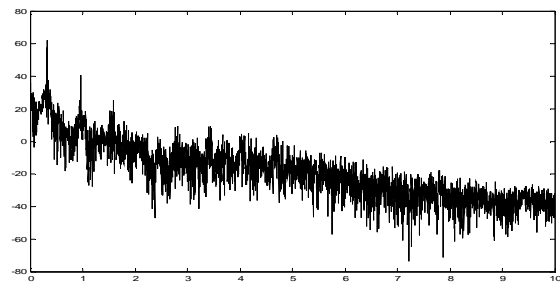
The power spectrum of system (6) with different k is obtained as shown in Fig.7.



(a) period orbit $k=0.55$



(b) quasi-period orbit $k=0.71$



(c) chaotic attraction $k=0.795$

Fig. 7 Signal spectrum of system (6)

7. Conclusions

In this paper, a memristor oscillator, which is extended from a Chua's oscillator by replacing the Chua's diode with a charge-controlled memristor, is presented and studied. This chaotic circuit uses only the four basic circuit elements, and has only one negative element in addition to the nonlinearity. The resulting chaotic system is not only demonstrated by computer simulations but also verified with Lyapunov exponents, bifurcation, poincaré mapping and power spectrum analysis.

Acknowledgement

This research was supported by the National Natural Science Foundation of China (Grant No. 60971022 and No. 61004078), National Natural Science Foundation of

Shandong Province, China (Grant No. ZR2009GM005 and ZR2009GQ009).

References

- [1] L.O. Chua, Memristor-the missing circuit element, *IEEE Transactions on Circuit Theory*, vol.CT-18, no.5, pp.507-519, 1971.
- [2] D.B. Strukov, etc. The missing memristor found, *Nature*, vol.453, pp80-83, 2008.
- [3] J.M. Tour and T. He, The fourth element, *Nature*, vol.453, pp.42-43, May 1, 2008
- [4] S. Shin, K. Ki, and S. Kang, Memristor applications for programmable analog ICs, *IEEE Transactions on Nanotechnology*, issue: 99,2010
- [5] K. Miller, etc, Memristive behavior in thin anodic titania, *IEEE Electron Device Letters*, vol.31, no.7, pp.737-739, July 2010
- [6] William M.Tong, etc., Radiation hardness of Tio2 memristive junctions, *IEEE Transactions on Nuclear Science*, vol.57, no.3, pp.1640-1643, June 2010
- [7] M. Vujisic, etc., Simulated effects of proton and ion beam irradiation on titanium dioxide memristors, *IEEE Transactions on Nuclear Science*, vol.57, no.4, pp.1798-1804, 2010
- [8] J. Borghetti, etc, Memristive switches enable stateful logic operations via material implication, *Nature*, vol.464, pp.873-876, 2010
- [9] Makoto Itoh and L.O.Chua, Memristor oscillators, *International Journal of Bifurcation and Chaos*, vol.18, no.11, pp.3183-3206, 2008
- [10] Daniel Batas and Horst Fiedler, A memristor spice implementation and a new approach for magnetic flux controlled memristor modeling, *IEEE Trans. on Nanotechnology*, vol.1, pp.1, 2010
- [11] Iv Petra, Fractional-order memristor-based Chua's circuit, *IEEE Transactions on Circuits and System*, vol.57, no.12, pp.975-979, 2010
- [12] D.Biolek and V.Biolkova, Mutator for transforming memristor into memcapacitor, *Electronics Letters*, vol.46, no.21, pp.1-2, October 2010.
- [13] Leon O. Chua. Canonical realization of Chua's circuit family. *IEEE Transaction on circuits and systems*, vol. 37, No. 7, July 1990.