# Chaotic Circuit Using a Ring Oscillator and a van der Pol Oscillator 

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#### Abstract

In this study, we propose a chaotic circuit using a ring oscillator and a van der Pol oscillator. The structure of this circuit is very simple. Therefore, IC implementation of this circuit is not so difficult. The exact solution and its Poincaré map are derived and the largest Lyapunov exponent are calculated. As a result, chaotic phenomena are confirmed. This chaotic circuit is suitable for using investigations of coupled chaotic systems, because this circuit is implemented as a electric circuit and IC implementation is not so difficult.


## 1. Introduction

Electric circuits are one of the natural physical system and it can be represented by a differential equations. Namely, it is considered that electric circuits are corresponding to many kinds of natural phenomena and it is expected that these phenomena be analyzed by using differential equations. Especially, electric circuits are suitable for investigating large scale chaotic coupled systems. Many investigation results can be obtained by circuit experiments immediately. Generally, large scale coupled systems can be described by high dimensional equations. Therefore, its analysis is not so easy and its computer simulation needs a long time. Additionally, the value of electric circuit elements is comparatively stable. If many same circuits are made, almost circuits will behave the same. These are very important advantages of using electric circuits for investigations of coupled systems.

However, implementing large scale coupled systems is not so easy. The one solution of this problem is IC implementation. Though circuit experiments of the large scale coupled chaotic system become easy by IC implementation, many money and time are needed. Therefore, the chaotic circuit which can be implemented as IC and can be also implemented as normal electric circuit is needed.

In this study, we propose a chaotic circuit using a ring oscillator and a van der Pol oscillator. The ring oscillator included the circuit consists of Op-Amps and resistors. The structures of the ring oscillator and the van der Pol oscillator are very simple. Therefore, IC implementation of this circuit is not so difficult.

## 2. Circuit Model

### 2.1. Circuit Schematic

Figure 1 shows the schematic of the proposed circuit. The left side is a ring oscillator which consists of three inverters. Resistor $R_{v r 1}$ is for adjusting the amplitude. The right side is a van der Pol oscillator. The Op-Amp part works as a negative resistor. These two oscillators are coupled via diodes which is placed the center.

In order to propose the new chaotic circuit, we used our designing technique. This technique is that two oscillators are coupled via diodes. It is very simple. However, this technique can not determine the parameters. In this study, parameters are determined by circuit experiments.


Figure 1: Circuit schematic.

### 2.2. Circuit Model

In order to make clear the mechanism of generating chaos, the proposed circuit is modeled as follows. Figure 2 shows diodes model of the proposed circuit. Diodes are modeled as a piece-wise linear function shown in Fig 2 (b). Figure 3 shows the circuit model of the proposed circuit shown in Fig. 1. Inverters of the ring oscillator are modeled as a inverter including a time delay. All elements except diodes are modeled as linear elements. It means that diodes play a role as non-linearity which is needed for generating chaos. Additionally, analysis becomes easy because the elements except diodes are linear elements. This is the useful advantage for investigating large scale coupled systems. Using this model, circuit equations are described as

(a)

(b)

Figure 2: Diodes model.


Figure 3: Circuit model.
follows:

$$
\left\{\begin{align*}
C_{1} \frac{d v_{1}}{d t} & =-\frac{1}{R_{1}} v_{1}-G_{m} v_{3}, \\
C_{1} \frac{d v_{2}}{d t} & =-\frac{1}{R_{1}} v_{2}-G_{m} v_{1}, \\
C_{1} \frac{d v_{3}}{d t} & =-\frac{1}{R_{2}} v_{3}-G_{m} v_{2}-i_{d},  \tag{1}\\
C_{2} \frac{d v_{4}}{d t} & =\frac{1}{R_{3}} v_{4}-i_{1}+i_{d}, \\
L_{1} \frac{d i_{1}}{d t} & =v_{4},
\end{align*}\right.
$$

where

$$
\begin{equation*}
i_{d}=\frac{1}{r_{d}}\left\{\left.v_{3}-v_{4}+\frac{1}{2}\left(\left|v_{3}-v_{4}-V_{t h}\right|-\mid v_{3}-v_{4}+V_{t h}\right) \right\rvert\,\right\} . \tag{2}
\end{equation*}
$$

$$
\begin{array}{ll}
\mathbf{S}_{\mathbf{1}}: & x_{3}-x_{4}=1 \\
\mathbf{S}_{\mathbf{2}}: & -\alpha x_{2}-\beta x_{3}-\gamma \delta x_{4}+\delta x_{5}<0 \tag{9}
\end{array}
$$

The subspace $\mathbf{S}_{\mathbf{1}}$ corresponds to the boundary condition between $\mathbf{R}_{\mathbf{1}}$ and $\mathbf{R}_{\mathbf{2}}$, while the subspace $\mathbf{S}_{\mathbf{2}}$ corresponding to the condition $\dot{x}_{3}-\dot{x}_{4}>0$. Namely, $\mathbf{S}$ corresponds to the transitional condition from $\mathbf{R}_{1}$ to $\mathbf{R}_{2}$.

Let us consider the solution starting from an initial point on $\mathbf{S}$. The solution returns back to $\mathbf{S}$ again after wandering $\mathbf{R}_{\mathbf{1}}, \mathbf{R}_{\mathbf{2}}$ and $\mathbf{R}_{\mathbf{3}}$. Hence, we can derive the Poincaré map as follows.

$$
\begin{equation*}
\mathbf{T}: \quad \mathbf{S} \rightarrow \mathbf{S}, \quad \mathbf{x}_{\mathbf{0}} \mapsto \mathbf{T}\left(\mathbf{x}_{\mathbf{0}}\right) \tag{10}
\end{equation*}
$$

where, $\mathbf{x}_{\mathbf{0}}$ is an initial point on $\mathbf{S}$, while $\mathbf{T}\left(\mathbf{x}_{\mathbf{0}}\right)$ is the point at which the solution starting from $\mathbf{x}_{0}$ hits $\mathbf{S}$ again. $\mathbf{T}\left(\mathbf{x}_{\mathbf{0}}\right)$ can be derived by using the exact solutions. The Jacobian matrix DT of the Poincaré Map T can be also derived.

We can calculate the largest Lyapunov exponent by

$$
\begin{equation*}
\mu=\lim _{N \rightarrow \infty} \frac{1}{N} \sum_{j=0}^{N} \log \left|\mathbf{D} \mathbf{T}_{j} \cdot e_{j}\right| \tag{11}
\end{equation*}
$$

here $e_{j}$ is a normalized base.

## 4. Circuit Experiments And Computer Calculations

Figure 4 shows the results of circuit experiments and computer calculated results. Circuit experiment results are shown in Fig. 4 (1). Projections of attractors onto $x_{3}$ and $x_{4}$ and their Poincaré maps are shown in Figs 4 (2) and (3), respectively. Rows of Fig. 4 are corresponding to each others. In circuit experiments, circuit parameters are fixed as $R=74.0[\mathrm{k} \Omega], C=0.015[\mu \mathrm{~F}], L=10[\mathrm{mH}], R_{n}=2.7[\mathrm{k} \Omega]$ and $R_{v r 2}=380[\mathrm{k} \Omega]$. The Op-Amp is type TL082. Corresponding parameters of computer calculations are fixed as $\alpha=4, \gamma=3, \delta=0.2, \varepsilon=100$ and $\zeta=0.5$. Control parameters are set as $R_{v r 1}$ and $\beta$. By changing control parameters, periodic orbits are observed in Figs. 4 (a), (b) and (f). Chaotic phenomena are observed in Figs. 4 (c) - (e).

One-parameter bifurcation diagram and the calculated largest Lyapunov exponent are shown in Fig. 5 and Fig. 6, respectively. Bifurcation phenomena of periodic orbits, chaos, window and so on are observed. For $4.20<$ $\beta<6.80$, the largest Lyapunov exponent becomes positive though two large windows are observed. By using this result, We can say the generation of chaos is confirmed numerically. By changing other parameters, period doubling bifurcation phenomena, tours and so on are also observed in this circuit.

## 5. Conclusions

In this study, we proposed the chaotic circuit using a ring oscillator and a van der pol oscillator. By circuit experiments, chaotic attractors have been observed and by using a linearized model, the generation of chaos have been confirmed numerically.

The characteristic of this circuit is a suitable for IC implementation and it is easy to implement as a normal electric circuit. This characteristic is useful for investigations of large scale coupled chaotic circuits. Therefore, it is expected that this circuit contributes to studies of large scale coupled chaotic circuits.

$R_{v r 1}=55[\Omega]$
(b)
(c)

$R_{v r 1}=148[\Omega]$
(1)

$\beta=6.7$

$\beta=4.0$
(2)
(3)

Figure 4: Circuit experiments and Computer calculation results. (1) Circuit experiments. (2) Projections of attractors. (3) Poincaré maps. Circuit experiments: Horizontal axis is $v_{3}[1.0 \mathrm{~V} / \mathrm{div}]$. Vertical axis is $v_{4}[1.0 \mathrm{~V} / \mathrm{div}]$. $R=74.0[\mathrm{k} \Omega], C=0.015[\mu \mathrm{~F}], L=10[\mathrm{mH}], R_{n}=2.7[\mathrm{k} \Omega]$ and $R_{v r 2}=380[\mathrm{k} \Omega]$. Computer calculations: $\alpha=4, \gamma=3$, $\delta=0.2, \varepsilon=100$ and $\zeta=0.5$.


Figure 5: One-parameter bifurcation diagram. Horizontal: $\beta$. Vertical: $x_{1}$


Figure 6: Largest Lyapunov exponents. Horizontal: $\beta$. vertical: $\mu$.

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