



# AWAD 2025 in Nara, JP



2025 Asia-Pacific Workshop on Advanced Semiconductor Devices (AWAD)

<https://www.ieice.org/es/sdm/jpn/awad2025/>

July 3–4, 2025

Kinsho hall, Todai-ji temple, Nara, Japan

## First Call for Papers

2025 Asia-Pacific Workshop on Advanced Semiconductor Devices (AWAD 2025) will be held from July 3 (Thu) to 4 (Fri), 2025. This year marks the 32<sup>nd</sup> of the AWAD, which has been held alternately in Japan and Korea since 1993 as the Workshop on Advanced LSI's and Devices, and as the Asia-Pacific Workshop on Fundamentals and Applications of Advanced Semiconductor Devices since 2000. The workshop has been renamed as the Asia-Pacific Workshop on Advanced Semiconductor Devices with the 30<sup>th</sup> anniversary. The purposes of this workshop are to bring scientists and engineers together, actively engaged in advanced semiconductor devices and materials, and to discuss the present and future device processing and related technologies. The workshop will cover the entire field of semiconductor devices and materials, from fundamental physics to recent improvements in device performance and processing technology.

### Sponsored by

The Institute of Electronics and Information Engineers (IEIE), in cooperation with Electronics Society, The Institute of Electronics, Information and Communication Engineers of Japan (IEICE-ES).

### Topics to be discussed will include, but not necessarily be limited to, the following areas:

- Advanced semiconductor technologies
- Characterization and simulation
- Compound semiconductor and wide bandgap devices
- Nano, 2D materials, wearable devices
- Neuromorphic devices and computing technologies
- MOS logic and memory devices
- Quantum devices and computing technologies
- Optoelectronics, displays, and imagers
- Power electronic devices

### Abstract submission

Prospective authors must submit 2-page maximum camera-ready paper (including figures and tables) for the technical digest with author information via the workshop website no later than **"Paper submission due" listed "Important Dates" below.**

### Venue

The workshop will be held at the Kinsho hall located at Todai-ji temple in Nara. \*From JR Nara Station/Kintetsu Nara Station, get off at the city circulation bus "Todaiji Daibutsuden/Kasugataisha-mae" stop and walk 5 minutes.

### Important Dates (KST, JST):

Paper submission start	Mar. 8, 2025
Paper submission due	<del>Apr. 13, 2025</del> <del>Apr. 27, 2025 (Extended)</del> <b>May 7, 2025 (Re-extended; the final due)</b>
Acceptance notification	May 31, 2025
Author registration	May 31, 2025
Advance registration due	June 15, 2025

#### For inquiries from Japan and other countries:

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## IEICE Transactions on Electronics (IEICE-C) / Special issues in Journal of Semiconductor Technology and Science (JSTS)

Authors are encouraged to submit their extended manuscript to the special issue either in 'Journal of Semiconductor Technology and Science' or 'IEICE Transactions on Electronics' (SCIE journals). The submitted papers will be published via a peer-review process. Detailed information regarding the submission procedures will be announced at the workshop and conference website.

### Language

The official language of the workshop is English.

### Registration Fee (tentative)

	Regular (JPY)	Student (JPY)	Senior (JPY)
Advance registration (until June 15, 2025)	30,000	15,000	15,000
Regular registration (after June 15, 2025)	40,000	20,000	20,000

\* Detailed information about registration is available at the workshop website: <https://www.ieice.org/es/sdm/jpn/awad2025/>

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### Young Researcher Award

Young Researcher Award and Young Researcher Poster Award will be given to young researchers who authored and presented outstanding oral/poster presentations at AWAD2025.

1. Candidate should be the first author who is a student (checked "student" in his/her paper submission form at initial submission) and present his/her paper by him/herself.
2. Young Researcher Award recipients are decided by the AWAD committee members on the basis of the evaluation of the manuscript for the technical digests and presentation. The number of award recipients is two.
3. Each winner will receive a certificate and a gift.
4. The award ceremony will be held at the closing session on the final day. Each winner will be awarded by the workshop chairs.