

Message from Vietnam Section Chair

The joint English session between the IEICE Electronics Society and the Vietnam Section, held on the 20th of March 2026, in Fukuoka, Japan, marked a significant milestone in advancing international collaboration within IEICE. As the Chair of the Vietnam Section and co-organizer of this session, I consider this initiative not only a successful academic exchange but also a strategic step toward strengthening the global engagement of IEICE and its international sections.

Under the theme “Collaborative Frontiers in Electronics,” the session brought together leading researchers from Japan and Vietnam to share recent advances in key areas such as hardware accelerator design, lightweight cryptography for IoT, neuromorphic systems, and emerging optical computing paradigms for edge AI. The four invited talks demonstrated both the depth and diversity of ongoing research, while also highlighting complementary strengths between the two countries. The presence of distinguished speakers from universities in Japan and Vietnam created a dynamic environment for knowledge exchange and mutual inspiration.

From the perspective of the Vietnam Section, this joint session represents an important opportunity to elevate our role within the IEICE community. It provided a platform for Vietnamese researchers to present their work in an international forum, engage directly with leading experts, and explore potential collaborations. At the same time, it allowed Japanese researchers to gain insights into emerging research activities and priorities in Vietnam, thereby opening new avenues for joint projects, student exchanges, and institutional partnerships.

More broadly, this initiative aligns closely with the globalization vision of IEICE. Establishing regular joint sessions between international sections and IEICE societies can serve as an effective mechanism to foster cross-border collaboration, enhance research visibility, and build sustainable academic networks. In particular, for rapidly developing regions such as Vietnam, such interactions are essential for capacity building, technology transfer, and integration into the global research ecosystem.

This first joint session can be seen as a pilot model for future activities. Its success demonstrates the feasibility and value of closer cooperation between IEICE and its international sections. Moving forward, we envision expanding this collaboration through co-organized workshops/conferences, joint special issues on IEICE transactions, and collaborative research programs that address shared challenges in electronics and information technology (such as NEXUS program).

In conclusion, the joint session in Fukuoka has laid a strong foundation for deeper collaboration between the Vietnam Section and the broader IEICE community. It not only strengthened academic ties between Vietnam and Japan but also contributed to the broader mission of IEICE in promoting internationalization and innovation in electronics, semiconductor, and Information Technology. We look forward to building upon this momentum and further enhancing the impact of such collaborative initiatives in the years to come.

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