

Call for Papers

----- Special Section on Low-Power and High-Speed Chips -----

The IEICE (Institute of Electronics, Information and Communication Engineers) Transactions on Electronics announces a forthcoming special section on Low-Power and High-Speed Chips to be published in **June 2024**.

The objective of the special section is to discuss advancement of low-power and high-speed chips and leading-edge technologies in all areas of microprocessors and their applications. The special section solicits paper submission from anyone in this field. The authors working in this area are strongly encouraged to submit original research papers.

1. Scope

The major topics of interest include, but not limited to:

- Low Power-High Performance Processors for AI/Machine Learning, IoT, Multimedia, Digital Consumer Electronics, Mobile, Graphics, Encryption, Security, Robotics, Automotive, Networking, Remote Systems, Medical, Healthcare, and Biometrics.
- Novel Architectures and Schemes for Single Core, Multi/Many-Core, NoC, Embedded Systems, Reconfigurable Computing, Grid, IoT, Dependable Computing, and Wireless/wired communication.
- Cool Software including Parallel Schedulers, Embedded Real-time Operating System, Binary Translations, Runtimes, Low-Power Algorithms, Compiler Issues and Low Power Application Techniques.

2. Submission Instructions

The deadline for submission is June 16, 2023 (Japan Standard Time). The standard number of pages is 8 for a PAPER. The maximum number of pages of a Brief Paper is 4. The article processing charge is considerably higher for extra pages. Manuscripts should be prepared according to the guideline in the "Information for Authors". The latest version is available at the web site, https://www.ieice.org/eng/shiori/mokuji_es.html. Note that the submitted manuscripts are required to cite 30 or more references for full paper and 15 or more references for brief paper, and that papers with fewer references will be rejected. The term for revising the manuscript after acknowledgement of conditional acceptance for this special section could be shorter than that for regular issues (60 days) because of the tight review schedule.

This special section will accept only papers by electronic submission. Prospective authors are requested to follow carefully the submission process described below.

Submit a paper using the IEICE web site, https://review.ieice.org/regist/regist_baseinfo_e.aspx. Authors should choose the [Special-LH] Low-Power and High-Speed Chips as a "Journal / Section" on the online screen. Do not choose [Regular-EC].

3. Inquiries:

Associate Prof. Ryuichi Sakamoto

Affiliation; Global Scientific Information and Computing Center, Tokyo Institute of Technology.

E-mail: r-sakamoto@gsic.titech.ac.jp

4. Special Section Editorial Committee

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 - * Upon accepted for publication, all authors, including authors of invited papers, should pay the article charges covering partial the cost of publication around November 2023. If payment is not completed by 15 December 2023, your manuscript will be handled as rejection.
 - * The accepted papers will be published online soon on the web site of Transactions Online after the payment of article processing charge has been completed. For detailed information, please visit https://www.ieice.org/eng/shiori/page2_es.html#8
 - * If there are non-members among the authors, we recommend that the authors take this opportunity to join the IEICE. For detailed information on the IEICE Membership Application, please visit the web-page, https://www.ieice.org/eng_r/join/individual_member.html.
- If all authors are non-members, the article processing charge for non-members will be applied, except for invited papers.
- *All papers published in or after August 2023 issue of the IEICE Transactions on Electronics are opened to all readers in the world through J-STAGE.