Call for Papers

Special Section on Recent Advances in Simulation Techniques and Their Applications for Electronics

The IEICE (Institute of Electronics, Information and Communication Engineers) Transactions on Electronics announces a forthcoming special section on Recent Advances in Simulation Techniques and Their Applications for Electronics scheduled for publication in November 2024.

Computer simulation plays an essential role in the design of systems, equipment, and their components and is also a powerful tool for assessing risks and safety problems. Moreover, it is used for state-of-the-art automatic discrimination by machine learning and deep learning. Simulation techniques for fields, electronic and optical devices, and systems are becoming increasingly important topics in our society. The purpose of this special section is to present a collection of original papers that provide an overview of recent progress in research and development of simulation techniques and their applications for electronics. Submission from researchers and engineers in this field is highly encouraged.

1. Scope
   The major topics of interest include:
   - Techniques on Computer Simulations for Electronic Engineering (Electromagnetics, Circuits, Semiconductors, Electronic Systems, etc.)
   - Hardware and Software Acceleration Techniques for Computer Simulations
   - High-Accuracy Techniques
   - Comparison and Estimation of Simulation methods (Canonical Problems for Comparison / Estimation)
   - Multi-Physics Simulations
   - Modeling and Optimization Techniques
   - Computer Platforms for Simulators
   - Simulators for Industrial and Educational Purposes
   - Machine Learning and Deep Learning for Simulation Techniques

2. Submission Instructions
   The submission deadline is October 21, 2023. The types of manuscripts for this Special Section are PAPER and BRIEF PAPER. Manuscripts should be prepared according to the style guidelines in the “Information for Authors” which is available at https://www.ieice.org/eng/shiori/mokuji_es.html. The length of PAPER should not exceed 8 printed pages. The length of BRIEF PAPER is limited to 4 printed pages for the initial submission. The term for revising the manuscript after acknowledgment of conditional acceptance for this special section could be shorter than that (60 days) for other sections because of the tight review schedule.

   This special section will only accept papers through electronic submission. Prospective authors are requested to follow carefully the submission process described below.

   (1) Submit a paper using the IEICE Web site https://review.ieice.org/regist/regist_baseinfo_e.aspx. (2) Choose “[Special-ES] Recent Advances in Simulation Techniques and Their Applications for Electronics” as a “Journal / Section.” Do NOT choose [Regular-EC].

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3. Special Section Editorial Committee
   Guest Editor-in-Chief: Masayuki Kimishima (Advantest Lab)
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   Authors must agree to the "Copyright Transfer, Article Processing Charge Agreement, Notices from the IEICE, and Privacy Policy" via electronic submission.

   Upon acceptance for publication, all authors, including those of invited papers, will be required to pay the page charges to partially cover the cost of publication around April 2024. If payment is not completed by May 15, 2024, your manuscript will be handled as rejection.

   The accepted papers will be published online soon on the web site of Transactions Online after the payment of article processing charges has been completed. For detailed information, please visit https://www.ieice.org/eng/shiori/page2_es.html#8.

   If there are non-members among the authors, we recommend that the authors take this opportunity to join the IEICE. For detailed information on the IEICE Membership Application, please visit the web page, https://www.ieice.org/eng_r/join/individual_member.html.

   If all authors are non-members, the article processing charge for non-members will be applied, except for invited papers.

   IEICE Transactions on Electronics requires authors to cite 30 or more references for full paper and 15 or more references for brief paper.

   All papers published in or after August 2023 issue of the IEICE Transactions on Electronics are opened to all readers in the world through J-STAGE.