The IEICE Transactions on Information and Systems announces that it will publish a special section entitled "Special Section on Parallel, Distributed, and Reconfigurable Computing, and Networking" in December 2020.

The objective of this special section is to publish and overview recent progress in the interdisciplinary area of Parallel, Distributed, and Reconfigurable Computing, and Networking. This special section will include papers based on the presentation at the International Symposium on Highly-Efficient Accelerators and Reconfigurable Technologies (HEART'19) and the International Symposium on Computing and Networking (CANDAR'19) in addition to papers applied for this call for papers. All submitted papers are subjected to the same review process as those papers accepted for publication in the regular issues.

1. **Scope**
This special section aims at timely dissemination of research in these areas. Possible topics include, but are not limited to:
- algorithms for parallel, distributed, and reconfigurable systems and applications (high-performance computing, IoT, machine learning, AI, image processing and computer graphics, data mining and information retrieval, multicore and accelerator-based computing, network algorithms, green computing, simulation and visualization, scheduling and load balancing, and performance model and evaluation)
- parallel, distributed, and reconfigurable systems and architectures (parallel processor architectures, cluster/grid systems, network and storage architectures, network-on-chip, high performance interconnect, FPGA, PLD, dynamic reconfiguration, and virtual hardware)
- software for parallel, distributed, and reconfigurable systems (operating systems, middleware, tools, virtualization, parallel programming models and languages, web services, cloud and distributed computing, and cluster/grid scheduling and resource management)
- distributed systems and networking (ubiquitous computing, P2P networks, wireless networks, and mobile computing)
- Technologies for reconfigurable systems (device and circuit, design and verification, high-level synthesis, energy-efficient, asynchronous, and reliable systems, and prototyping)

2. **Submission Instructions**
- A manuscript should be prepared according to the guideline given in “The Information for Authors” (https://www.ieice.org/eng/shiori/mokuji_iss.html). We encourage the authors to use the IEICE Style File (https://www.ieice.org/ftp/index-e.html). The preferred length of the manuscript is 8 pages for a PAPER and 2 pages for a LETTER with the format determined by the IEICE Style File.
- Submit the manuscript through the IEICE Web site (https://review.ieice.org/regist/regist_baseinfo_e.aspx). Choose “[Special-PA] Parallel and Distributed Computing and Networking” in the menu of “Journal/Section” in the submission page. Do not choose “[Regular-ED] Information and Systems” or other special sections.
- Authors must agree to the “Copyright Transfer and Page Charge Agreement” via electronic submission.
- Submission deadline of the manuscript is January 7, 2020. (No Extension)

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3. **Special Section Editorial Committee**
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* Upon accepted for publication, all authors, including authors of invited papers, should pay the page charges covering partial cost of publication around August 2020. If payment is not completed by 15 September, 2020 your manuscript will be handled as rejection.

* The standard period of 60 days between the notification (of conditional accept) and the second submission can be shortened according as the review schedule.

* At least one of the authors must be an IEICE member when the manuscript is submitted for review. Invited papers are an exception. We recommend authors unaffiliated with IEICE to apply for the membership (https://www.ieice.org/eng/join_ieice/index.html).

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