Annual Activity Report of IEICE Vietnam Section

Date of Report: 28 January 2019

Reporter: Xuan-Tu Tran, Vietnam National University, Hanoi

1. Organization

Assembly of the Section

- Rep. Office: Xuan-Tu Tran, Professor
- Deputy Rep.: Trong-Tu Bui, Professor
- Secretary: Duy-Hieu Bui, PhD
- Treasurer: Duy-Hieu Bui, PhD

2. Strategy of the Section

- Creating a friendly-open forum for exchanging academic and industrial experiences and new ideas in the fields of Information and Communication Technology.
- Promoting the collaborations between Vietnamese and Japanese scientists, institutions and companies through professional activities of IEICE.

3. Membership Development Policy

- Broadcasting the advantages of joining
- Joint members between IEICE and the Radio and Electronics Association of Vietnam (REV).
- Encourage scientists to publish their technical contributions to IEICE Transactions.

4. Results of Activities During the 2017/2018 Fiscal Year (April 2017 ~ March 2019)

4.1 Conference/Symposium/Workshop

- Organized the 2017 International Conference on Integrated Circuits, Design, and Verification (ICDV). This is a biennial event organized by the IEICE Vietnam Section and supported by the VLD committee since 2010. The ICDV 2017 was hosted by VNU University of Engineering and Technology.
- Organized the Vietnam Japan Microwave 2017 Conference, June 13-14th, Hanoi University of Science and Technology.
- Technical support for the 2018 International Conference on Multi-core/Many-core Systems on Chip (MCSoC 2018), hosted in Hanoi from 12 to 14 September 2018.

4.2 IEICE Sponsored Lecture (Including Participant Number)

- No sponsored lecture this year.

4.3 The Other Events in Collaboration with IEICE Hqs or Domestic Sections, if Available

- Technical support for the 2018 International Conference on Advanced Technologies for Communications (ATC 2018) in Ho Chi Minh city.
- The 1st South-East Asia workshop on Circuits and SEACAS workshop, 7-8 November 2017.
- Supporting several student teams to participate the LSI contest in Okinawa, Japan. In 2018, there are two teams (from Vietnam National University – Hanoi and Hanoi University of Science and Technology) got the Second Price at the 21th LSI Design Contest at Okinawa.

4.4 Improvement of the Section (Membership Evolution, Advertisement of Activities, Publication, etc.)

- Remain and slightly increase the number of members.
- Distribute and advertise the section’s activities through mailing list, social networks such as Facebook…
- Increase the publication of the section’s members at the conferences, workshops and IEICE journals.

5. Review and Future Plan

(1) Items in 2018 Fiscal Year Plan Which Could Not Be Achieved, and Expected Solution

- Maintaining many activities of the sections even there is a difficulty.

(2) Activity Plan in 2019 Fiscal Year

- Organize scientific events (conference, workshop, seminars): ICDV 2019, …
Sponsored Lectures/seminars on LSI Design, Communications, IoTs, Big Data/Data Science…
Electronic Design Contest in Vietnam and LSI contest in Okinawa, Japan.
Preferable topics to collaborate with regional Sections in Japan: LSI design, IoT applications, EDAs, Photonic Network, Microwave, Embedded Systems, Multilingual Processing and NLP, Big data/ data analysis…

6. Constructive Opinion/Request/Proposal

- Kindly offer yearly financial support to maintain and develop the activities of the Section.
- Kindly support to the organization of the International Conference on Integrated Circuits, Design, and Verification (ICDV).
- Kindly provide timely updated list of members registered directly to the Headquarter.
- Kindly give a list of Papers of IEICE Publications by Vietnamese members; a list of Vietnamese participants into the IEICE events in the end of each year (if possible).