

IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences

Special Section on “ VLSI Design and CAD Algorithms ”

(December 2012 Issue)

The IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences announces a forthcoming Special Section on VLSI Design and CAD Algorithms to be published in December 2012.

The objective of the special section is to discuss new theoretical or practical developments and techniques in VLSI design and CAD algorithms. The special section solicits paper submission from anyone in this field, and especially from people who present papers at ASP-DAC 2012 or SASIMI 2012. The authors working in this area are strongly encouraged to submit original research papers on the topics which include, but are not limited to, the following areas:

VLSI design methodology	VLSI architecture	Co-design	High-level synthesis
Logic synthesis	Low power design	Formal verification	Layout verification
Simulation (device, process, circuit, logic, high-level, etc.)	Test pattern generation	Design for testability	
Floor-planning and placement	Routing	Analog circuit design	Cell/module design
			System level design

Notes for Authors:

All manuscripts should be prepared according to the “Information for Authors” which is available in http://www.ieice.org/eng/shiori/mokuji_ess.html. It is recommended that the lengths of a paper and a letter for this special section are within 8 and 2 printed pages, respectively. For the submission, at least one author of each paper must be a member of the IEICE.

This special section will accept papers only by electronic submission. Prospective authors are requested to follow carefully the submission process described below.

1. Submit a complete paper and **electronic files (Word/TeX files, figures, authors’ photos and biography)** for publishing using the IEICE Web site https://review.ieice.org/regist_e.aspx. Authors should choose the “[Special-EA] VLSI Design and CAD Algorithms” as a “Type of Issue (Section)/Category of Transactions” on the online screen. Do not choose “[Regular-EA]”. See details on http://www.ieice.org/eng/shiori/mokuji_es.html
2. Send the following items to Prof. Tetsuya Hirose (Secretary) by postal mail, FAX or E-mail. Please notify “Special Section on VLSI Design and CAD Algorithms” and Temporary ID from the electronic submission system.
 - “Signed Copyright Transfer and Page Charge Agreement”
 - “Confirmation Sheet of Manuscript Registration”

The manuscript will undergo the standard review process of the IEICE Transactions on Fundamentals. However, please note that if revision is requested, you will be granted only 4 weeks to revise your manuscript.

Deadline of Submission:

March 15, 2012 (manuscript by electronic submission)

March 15, 2012 (signed copyright transfer and confirmation sheet to Prof. Hirose)

Submission and Further Information:

Prof. Tetsuya Hirose

Department of Electronic and Electrical Engineering, Kobe University

1-1 Rokko-dai, Nada, Kobe 657-8501, JAPAN

Phone: +81-78-803-6075, FAX: +81-78-803-6075

E-mail: hirose@eedept.kobe-u.ac.jp

Guest Editor: Masahiro Numa (Kobe Univ.)

Secretaries: Yoshinobu Higami (Ehime Univ.), Tetsuya Hirose (Kobe Univ.)

Guest Associate Editors:

Motoki Amagasaki (Kumamoto Univ.), Makoto Ikeda (Univ. of Tokyo), Tohru Ishihara (Kyoto Univ.), Kazuhito Ito (Saitama Univ.), Kenichi Okada (Tokyo Inst. of Tech.), Seiji Kajihara (Kyushu Inst. of Tech.), Mineo Kaneko (JAIST), Hiroshi Kawaguchi (Kobe Univ.), Tomoya Kitai (NEC), Shinji Kimura (Waseda Univ.), Atsushi Kurokawa (Hirosaki Univ.), Kazutoshi Kobayashi (Kyoto Inst. of Tech), Yuichiro Shibata (Nagasaki Univ.), Toshiyuki Shibuya (Fujitsu Lab.), Tian Song (Tokushima Univ.), Kenshu Seto (Tokyo City Univ.), Kazuyoshi Takagi (Kyoto Univ.), Yasuhiro Takashima (Univ. of Kitakyushu), Atsushi Takahashi (Osaka Univ.), Nozomu Togawa (Waseda Univ.), Hiroyuki Tomiyama (Ritsumeikan Univ.), Shigetoshi Nakatake (Univ. of Kitakyushu), Yuichi Nakamura (NEC), Masanori Hashimoto (Osaka Univ.), Kiyoharu Hamaguchi (Osaka Univ.), Hiroyuki Higuchi (Fujitsu), Yukiya Miura (Tokyo Metropolitan Univ.), Shin-ichi Minato (Hokkaido Univ.), Fumihito Minami (STARC), Shigeru Yamashita (Ritsumeikan Univ.), Akihisa Yamada (Sharp), Yasushi Yuminaka (Gunma Univ.), Masaya Yoshikawa (Meijyo Univ.), Takayuki Watanabe (Univ. of Shizuoka)

Please note that if the manuscript is accepted for publication, all authors should pay the page charges covering partial cost of publication. Authors will receive 50 copies of the reprint.